Weekly Progress Report

T12

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Week ending 03/22/2015

**Completed Tasks:**

1. Meet with professor Faust and Dr. Morris to review documentation.
2. Per request re-worked experiment Description and requirements (draft)
3. Came up with 3 possible patterns using excel
4. Began writing Verilog code to simulate LSFR patterns

**Tasks to Be Completed Next Week:**

1. Review LSFR timing to ensure max patterns are generated
2. Update PDS and requirements document (final).
3. Finish Verilog code and test bench to for LSFR
4. Get values for variables passive components

**Issues:**

Possible solution to issue use NXOR then delay…by hand provided 27/32 patterns